**IN THE CLAIMS**

Please cancel claims 48-56 without prejudice.

Please accept a new listing of the pending claims as follows:

1-37. (Cancelled)

38. (Previously Presented) A digital integrated receiver decoder comprising:

a plurality of front-ends, including at least a first front-end and a second front-end;

said first front-end being configured to receive a first bit stream from a first source and a second front-end being configured to receive a second bit stream from a second source;

a transport processor coupled to said first front-end and said second front-end, said transport processor being configured to process said first bit stream and said second bit stream and providing a first processed bit stream and a second processed bit stream in response to the first bit stream and the second bit stream respectively; and

at least one decoder coupled to said transport processor and configured to simultaneously select the first processed bit stream and the second processed bit stream for decoding.

39. (Previously Presented) The digital integrated receiver decoder of claim 38, wherein

said transport processor is configured to simultaneously select the first bit stream and the second bit stream for recording.

40. (Previously Presented) The digital integrated receiver decoder of claim 38, wherein

said first and second front-ends provide outputs to first and second demodulators, said first and second demodulators each being configured for a different mode of demodulation.

41. (Previously Presented) The digital integrated receiver decoder of claim 40, wherein

said integrated receiver decoder comprises more than two front-ends and wherein said transport processor is configured to select first and second front-ends and

wherein each front-end is associated with a differently modulated form of input signal.

42. (Previously Presented) The digital integrated receiver decoder of claim 40, wherein

said transport processor is configured to simultaneously select the first bit stream and the second bit stream for recording.

43. (Previously Presented) A digital television receiver comprising:

a plurality of tuners, including at least a first front-end and a second front-end;

said first front-end being configured to receive a first bit stream from a first source and a second front-end being configured to receive a second bit stream from a second source;

a transport processor coupled to said first front-end and said second front-end, said transport processor being configured to process said first bit stream and said second bit stream and providing a first processed bit stream and a second processed bit stream in response to the first bit stream and the second bit stream respectively; and

at least one decoder coupled to said transport processor and configured to simultaneously select the first processed bit stream and the second processed bit stream for decoding.

44. (Previously Presented) The digital television receiver of claim 43, wherein

said transport processor is configured to simultaneously select the first bit stream and the second bit stream for recording.

45. (Previously Presented) The digital television receiver of claim 43, wherein

said first and second front-ends provide outputs to first and second demodulators, said first and second demodulators each being configured for a different mode of demodulation.

46. (Previously Presented) The digital television receiver of claim 45, wherein

said digital television receiver includes

a plurality of front-ends and

wherein said transport processor is configured to select first and second front-ends and wherein each front-end is associated with a differently modulated form of input signal.

47. (Previously Presented) The digital television receiver of claim 46, wherein

said transport processor is configured to simultaneously select the first bit stream and the second bit stream for recording.

48-56. (Cancelled).